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| **사외 교육 보고서** | | | | | | |  |  | |  | | --- | | C:\Users\user\AppData\Local\Temp\hunclip1\02\huntemp.files\img0001.jpg | | | |
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| 팀 명 /  교 육 자 | | Automotive CM팀 | | | | | | ( 급 호 : 주 임, 사 원 ) | | |
| 김성연, 복준혁 | | | | | |
| 기 간 | | 2019년 03 월 18일 (월) 부터 | | | | | | | | |
| 2019년 03월 19일 (화) 까지 | | | | | | | | |
| 교 육 명 | | Zynq를 이용한 개발 입문 | | | | | | | | |
| 장 소 | | 서울특별시 강남구 문정동 송파대로 201 | | | | | | | | |
| 주관 업체 | | WeDu solution / TEL : 02-881-5131 | | | | | | | | |
| 교육 주제 | | Zynq EPP개요, ARM Cortex-A9 processor에 기초한 Processing system(PS) 및 Programmable Logic(PL) 이해 및 Zunq Board를 통한 실습 | | | | | | | | |
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| 교육 내용 | | | | | | | | | | |
| **1일차**   1. Building a Zynq all programmable SoC platform- examine the process of using the vivado IP integrator tool to create a simple processing system. 2. Integrating Programmable Logic on the Zynq All Programmable SoC – Connect a programmable logic (PL) design to the embedded processing system (PS). 3. Using DMA on the Zunq All Programmable SoC – Experiment with effectively using the PS DMA controller to move data between DDRx memory and a custom PL peripheral.   **2 일차**   1. Impact of Port Selection on System Performance – Explore bandwidth issues surrounding the use of the Accelerator Coherency Port (ACP) and the High Performance (HP) ports. 2. Debugging on the Zunq All Programmable SoC – Evaluate debugging the hardware and software components of a Zynq All Programmable SoC design. 3. Running and Debugging a Linux Application on the Zunq All Programmable SoC – Explore a software Application executing under the Linux operating system on the Zunq All Programmable SoC. | | | | | | | | | | |
| 상세 내용 | | | | | | | | | | |
| **1일차**   1. Building a Zynq all programmable SoC platform- examine the process of using the vivado IP integrator tool to create a simple processing system. 2. Integrating Programmable Logic on the Zynq All Programmable SoC – Connect a programmable logic (PL) design to the embedded processing system (PS). 3. Using DMA on the Zunq All Programmable SoC – Experiment with effectively using the PS DMA controller to move data between DDRx memory and a custom PL peripheral.   **2 일차**   1. Impact of Port Selection on System Performance – Explore bandwidth issues surrounding the use of the Accelerator Coherency Port (ACP) and the High Performance (HP) ports. 2. Debugging on the Zunq All Programmable SoC – Evaluate debugging the hardware and software components of a Zynq All Programmable SoC design. 3. Running and Debugging a Linux Application on the Zunq All Programmable SoC – Explore a software Application executing under the Linux operating system on the Zunq All Programmable SoC. | | | | | | | | | | |
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